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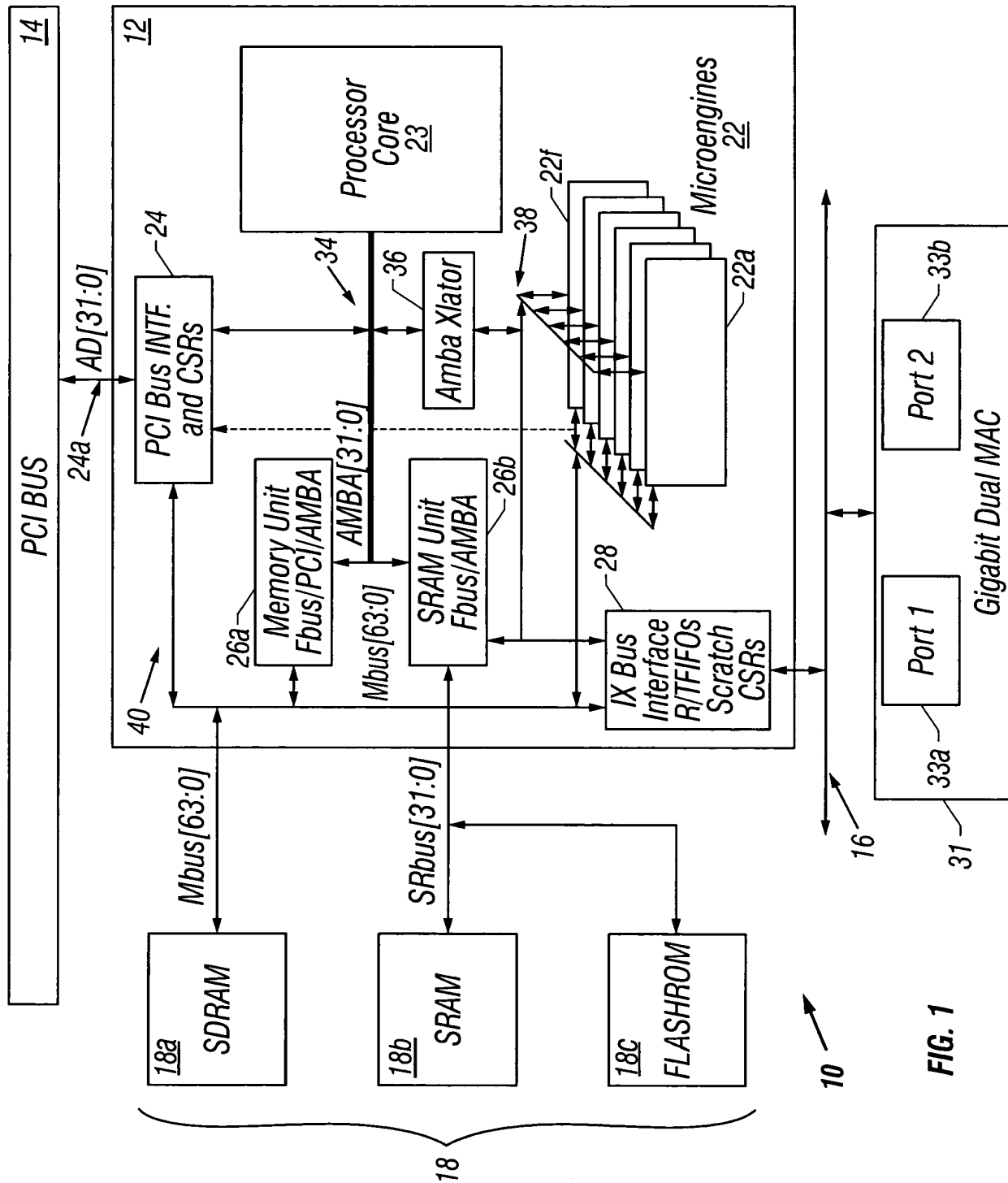
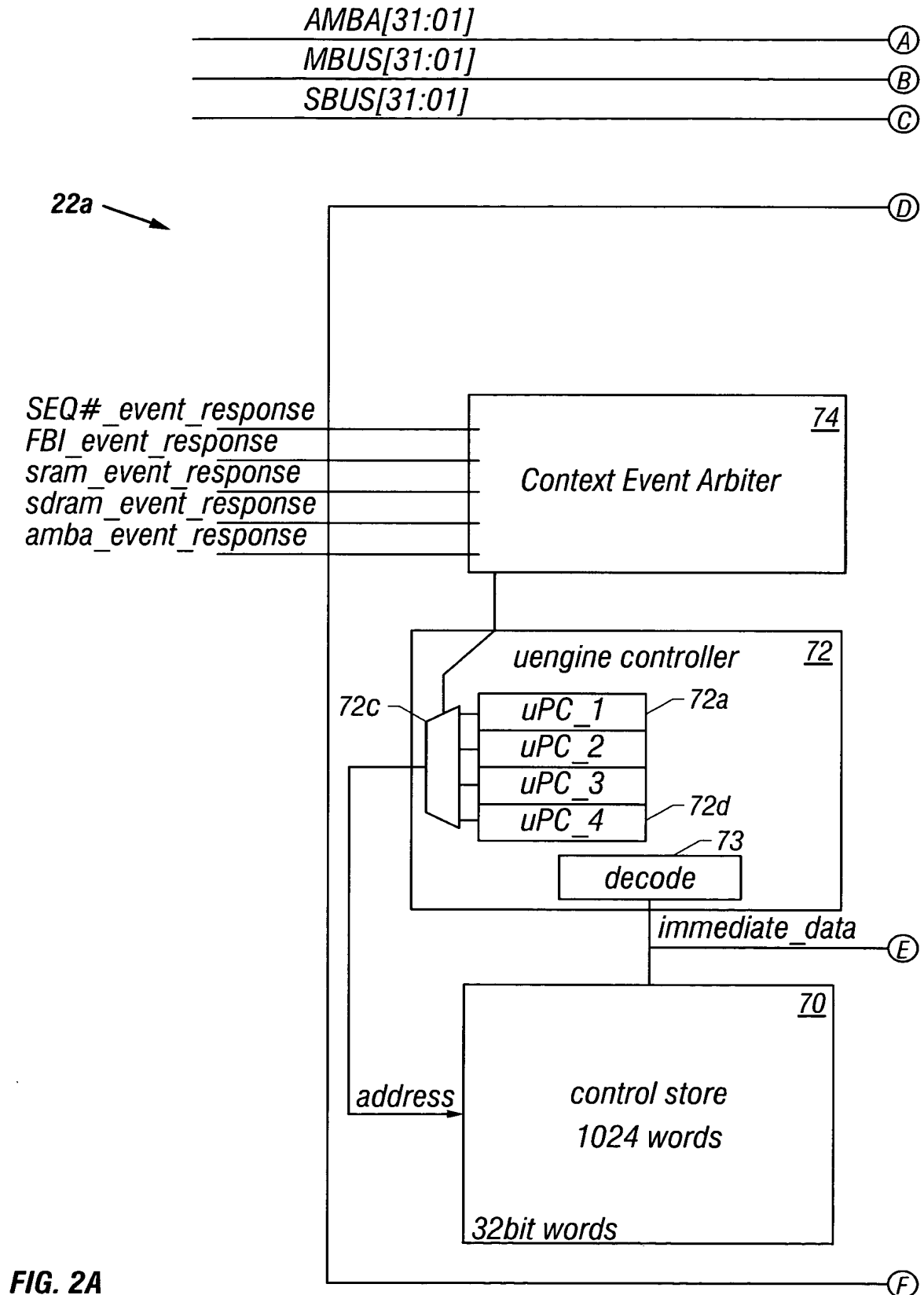
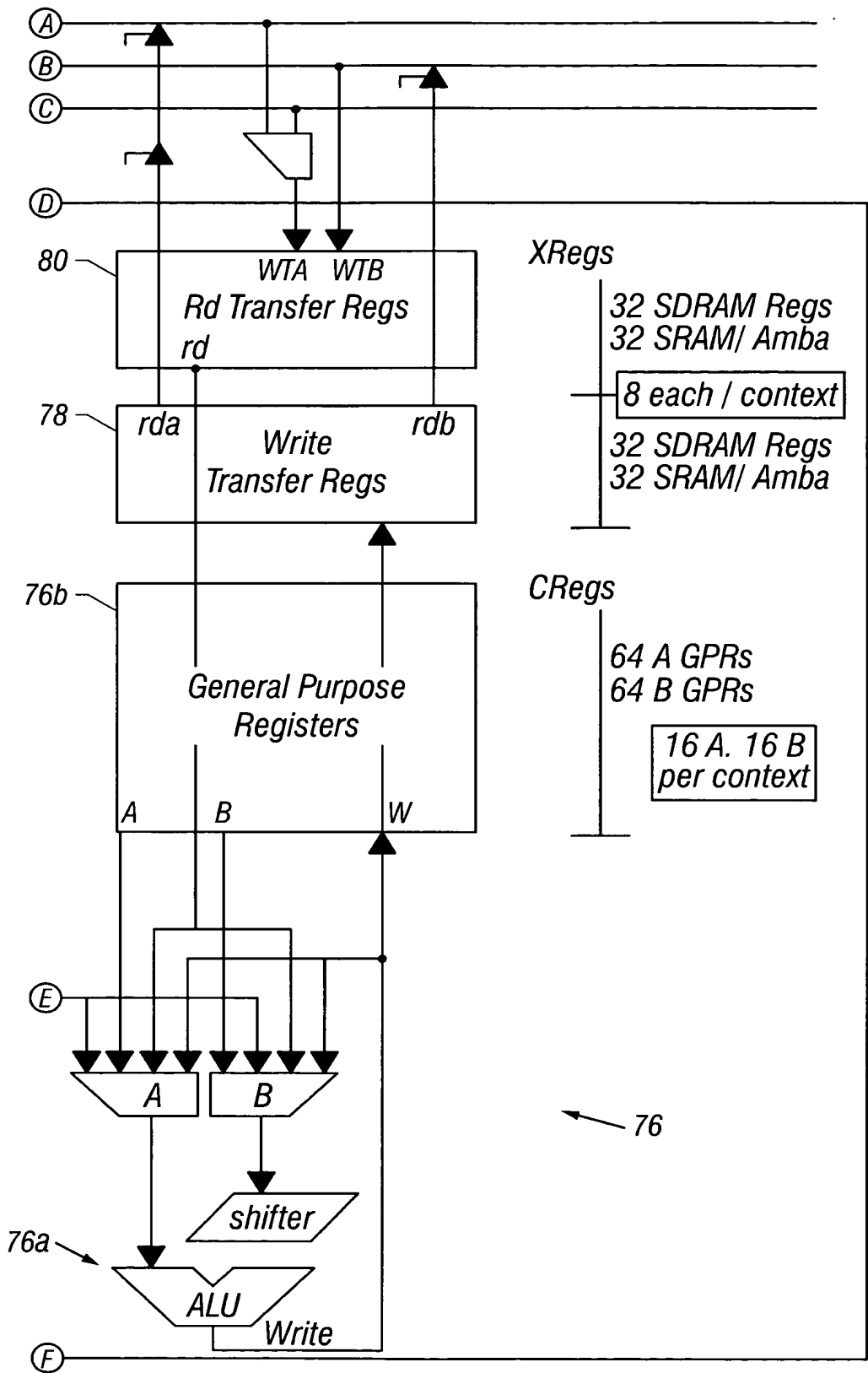


FIG. 1

2/20**FIG. 2A**

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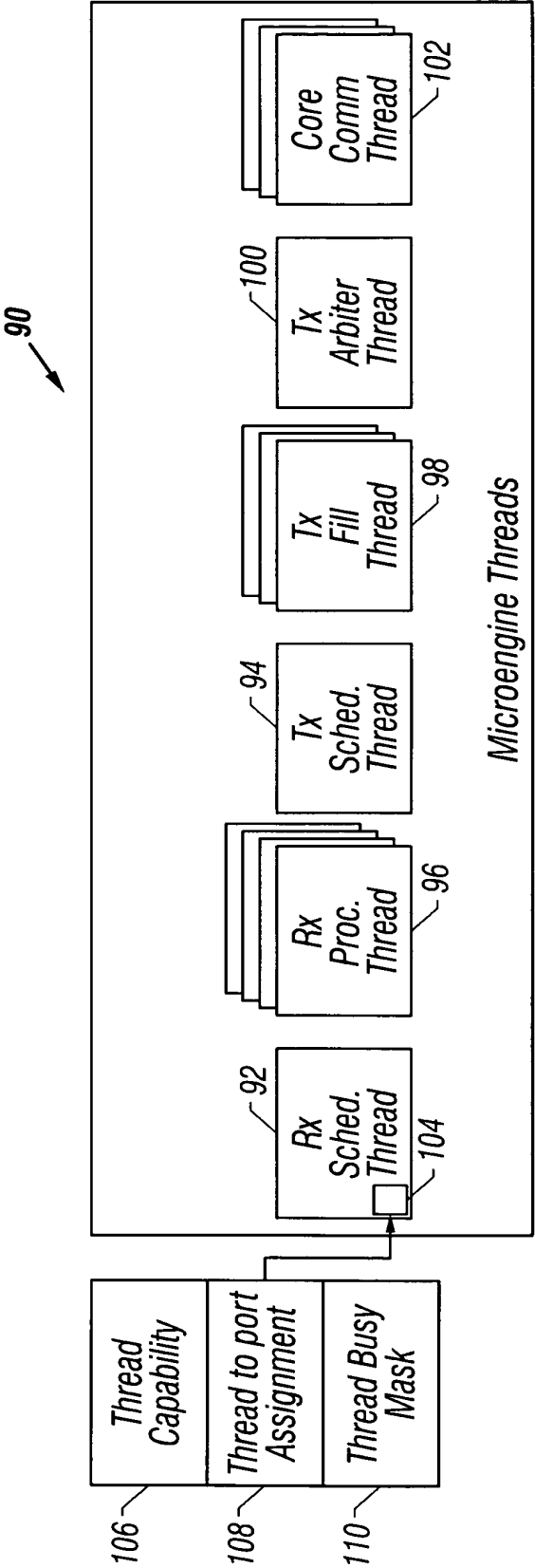


FIG. 3

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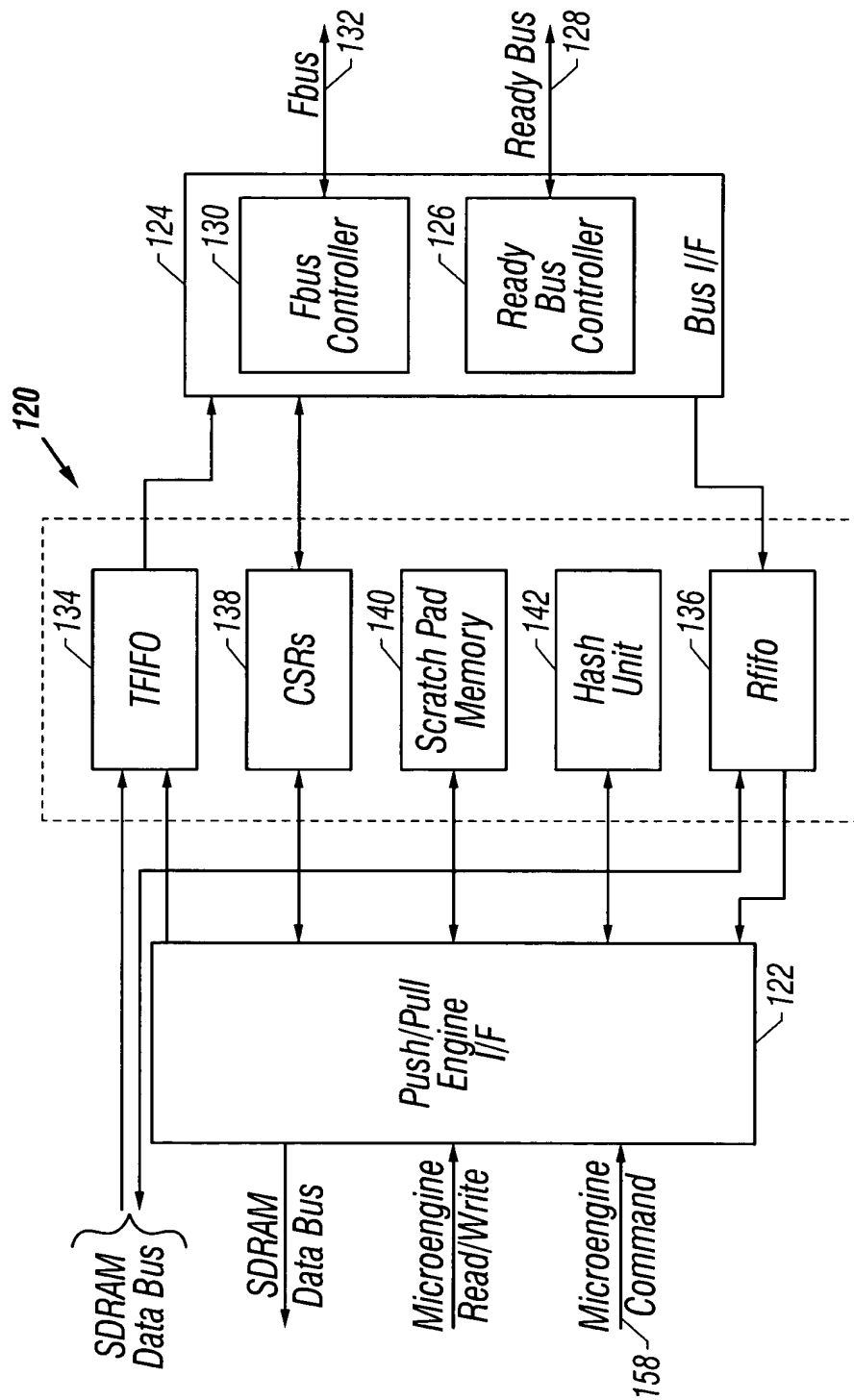


FIG. 4

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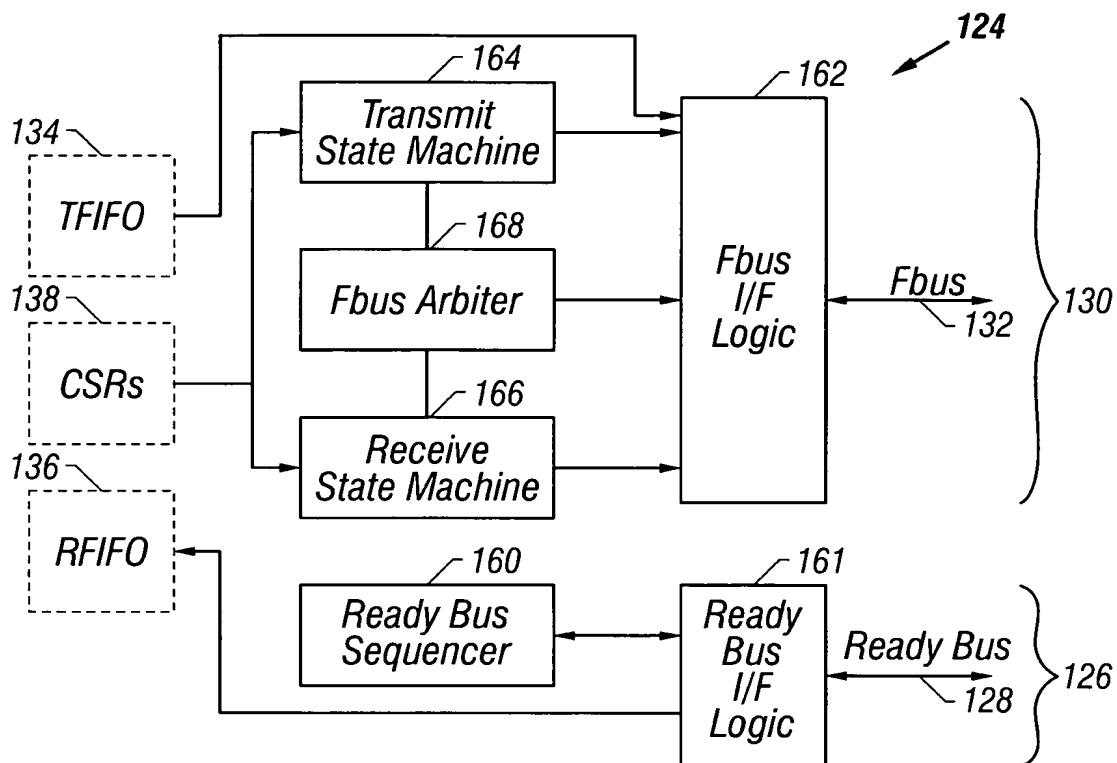


FIG. 5

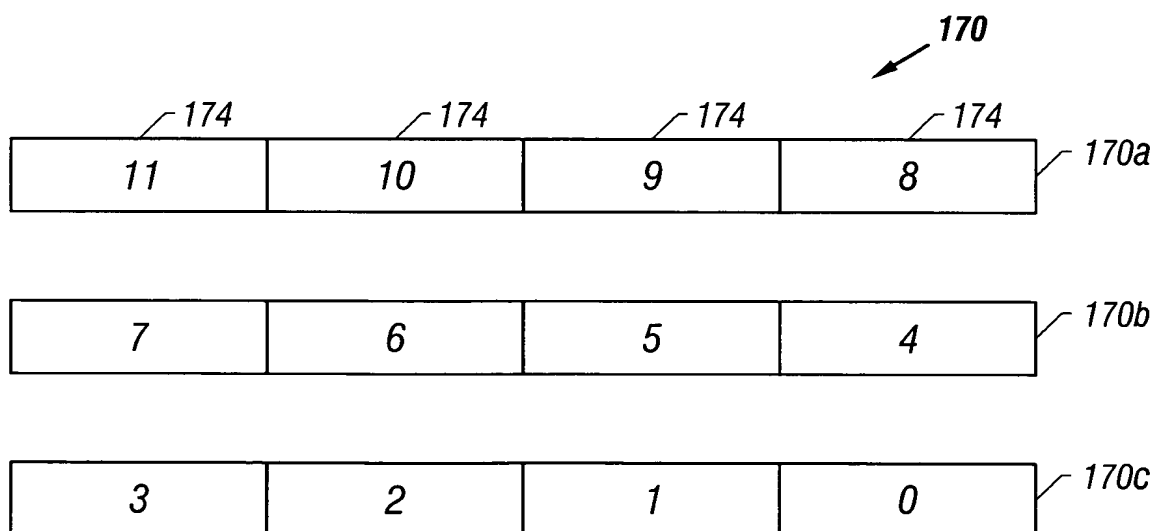


FIG. 6A

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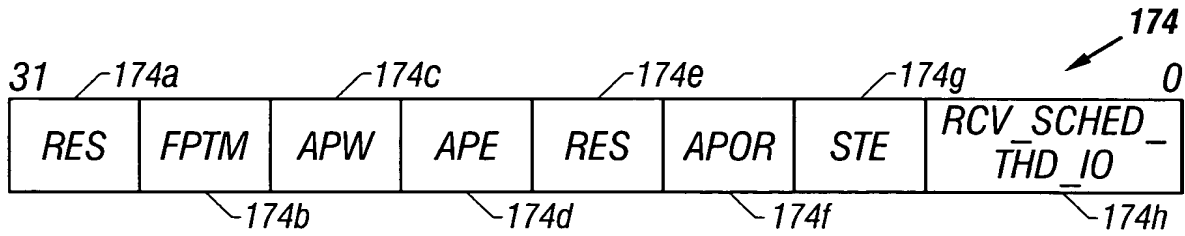


FIG. 6B

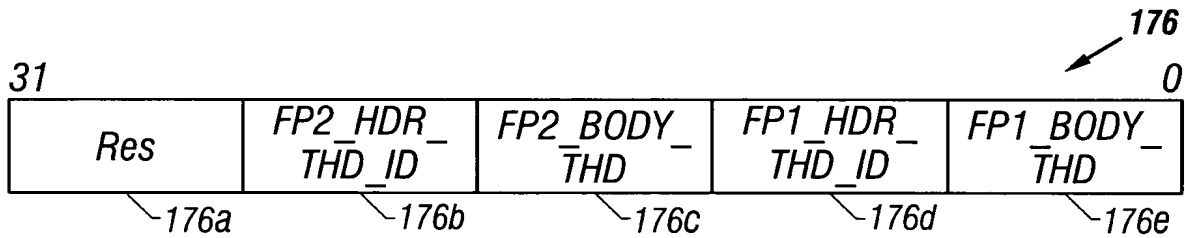


FIG. 6C

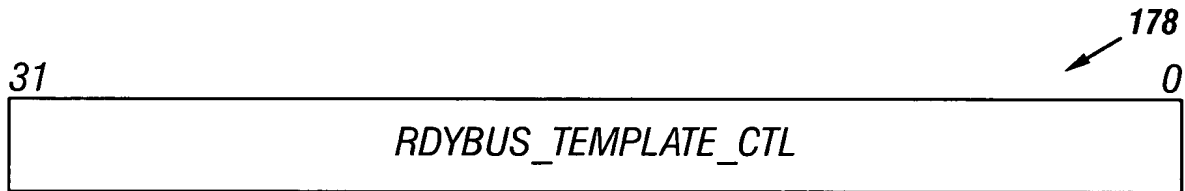


FIG. 6D

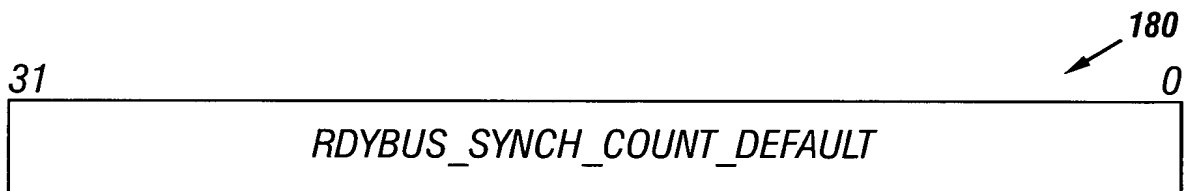


FIG. 6E



FIG. 6F

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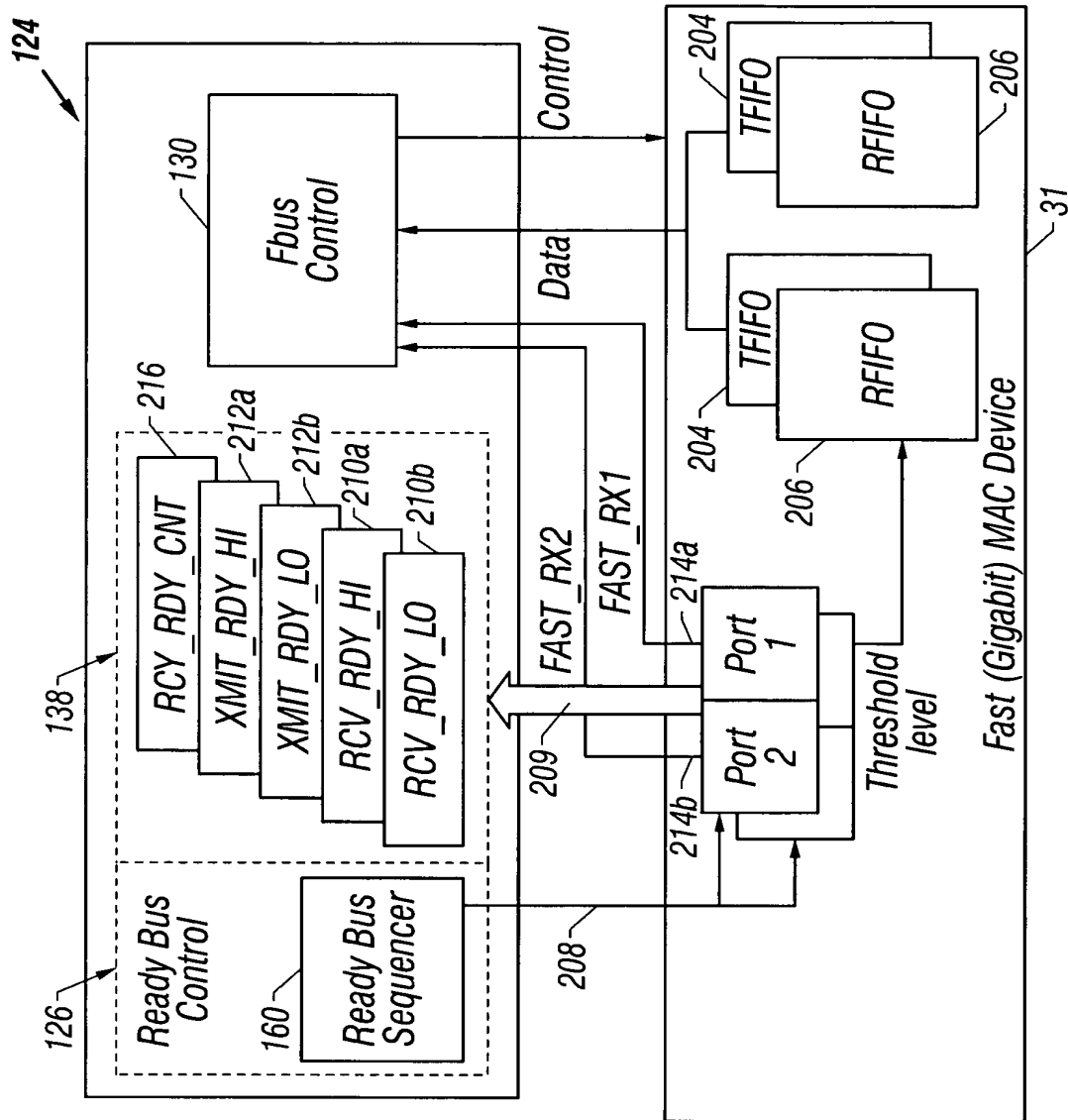


FIG. 7

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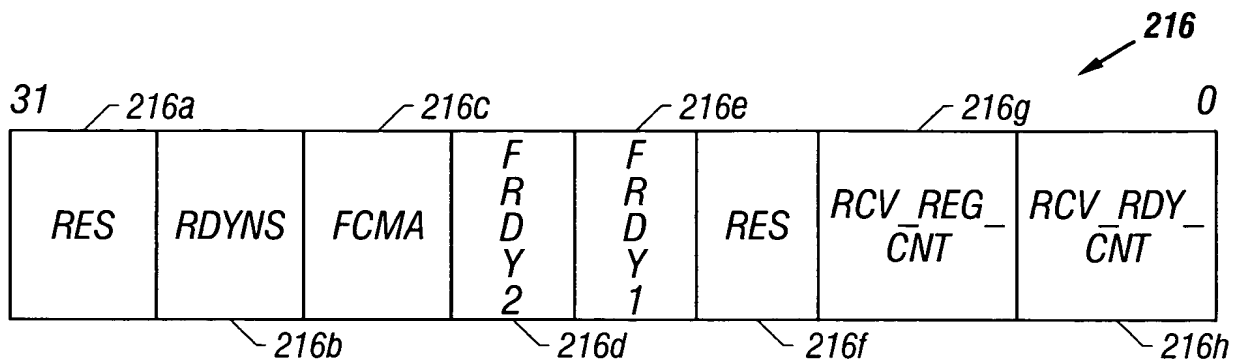


FIG. 8A

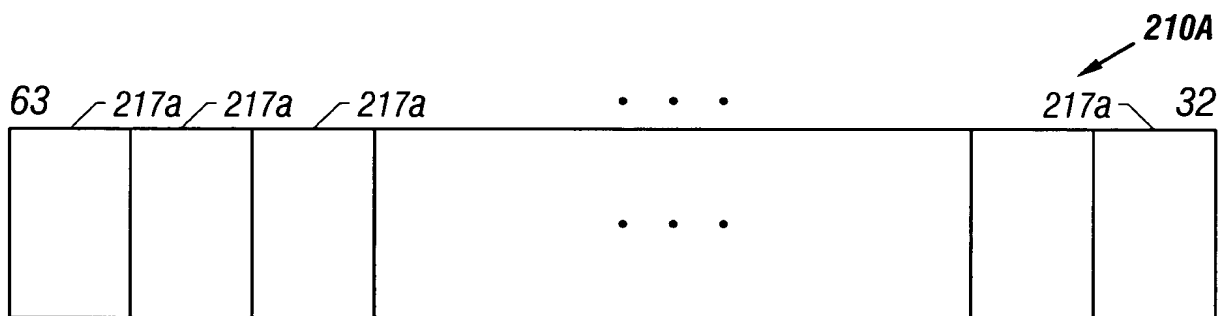


FIG. 8B

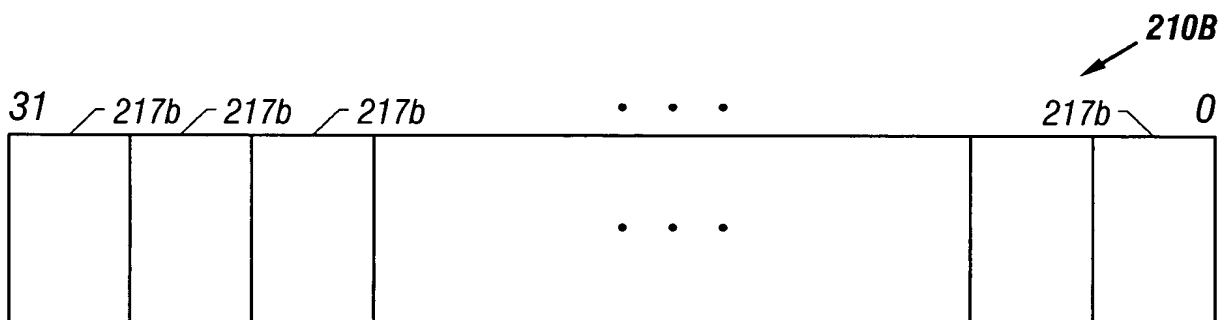


FIG. 8C

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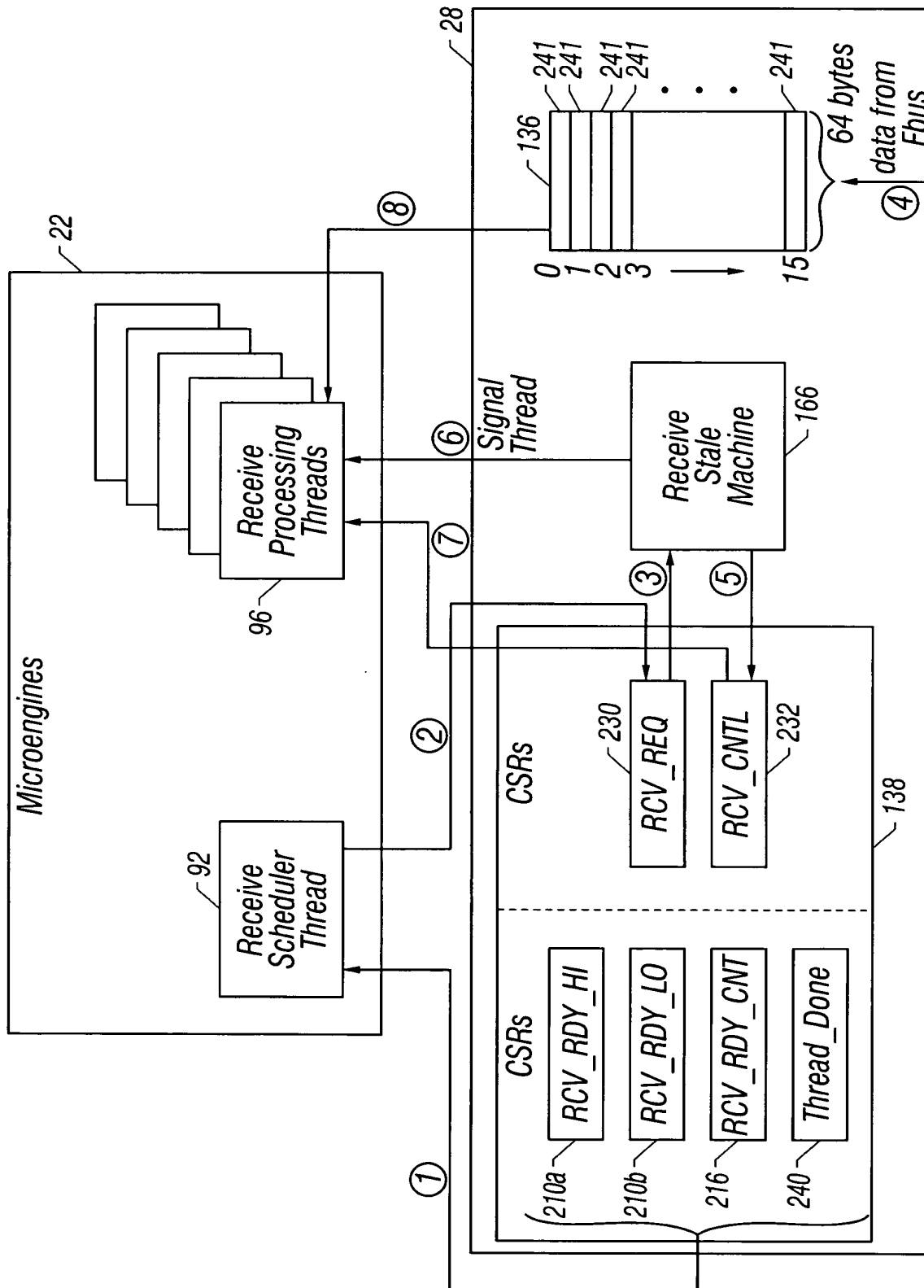


FIG. 9

230

230a	230B	230C	230d	230f	230e	230g	230h	230j	230i	230k	230l	230m
RES	FA	THSG	SL	E2	E1	FS	NFE	IGFR	SIGRS	TID	RM	RN
0												
1												

231

FIG. 10A

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232

232a	232b	232c	232d	232e	232f	232g	232h	232i	232j	232k	232l
THMSG	MAC- PORT/ THD	SOP SEQ#	RF	RERR	SE	FE	EF	SN	Valid Bytes	EOP	SOP
0											
1											
2											
3											

233

233

FIG. 10B

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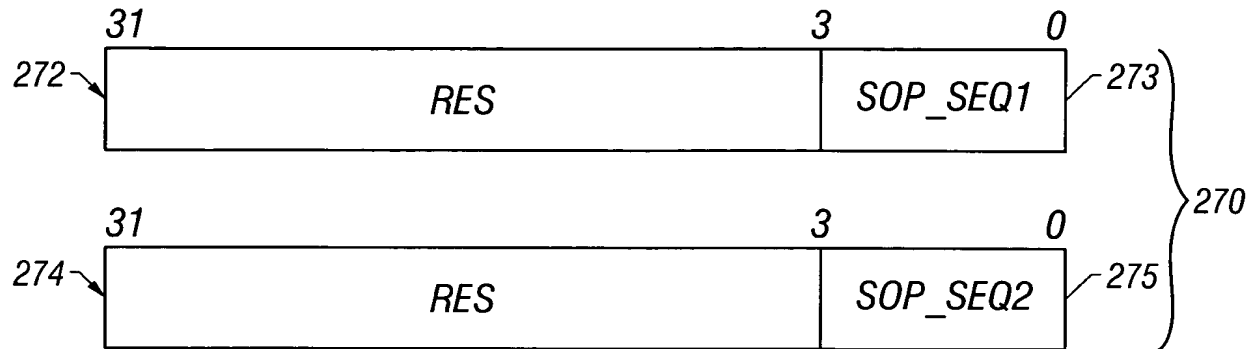


FIG. 11A

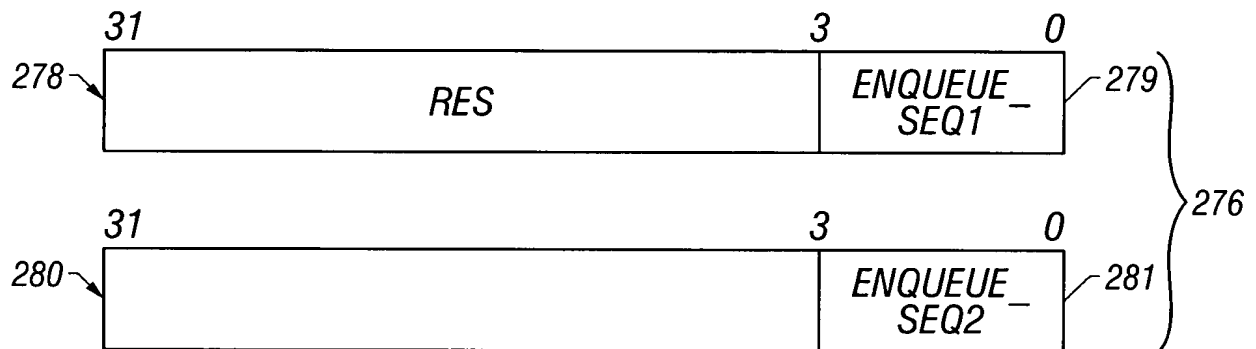


FIG. 11B

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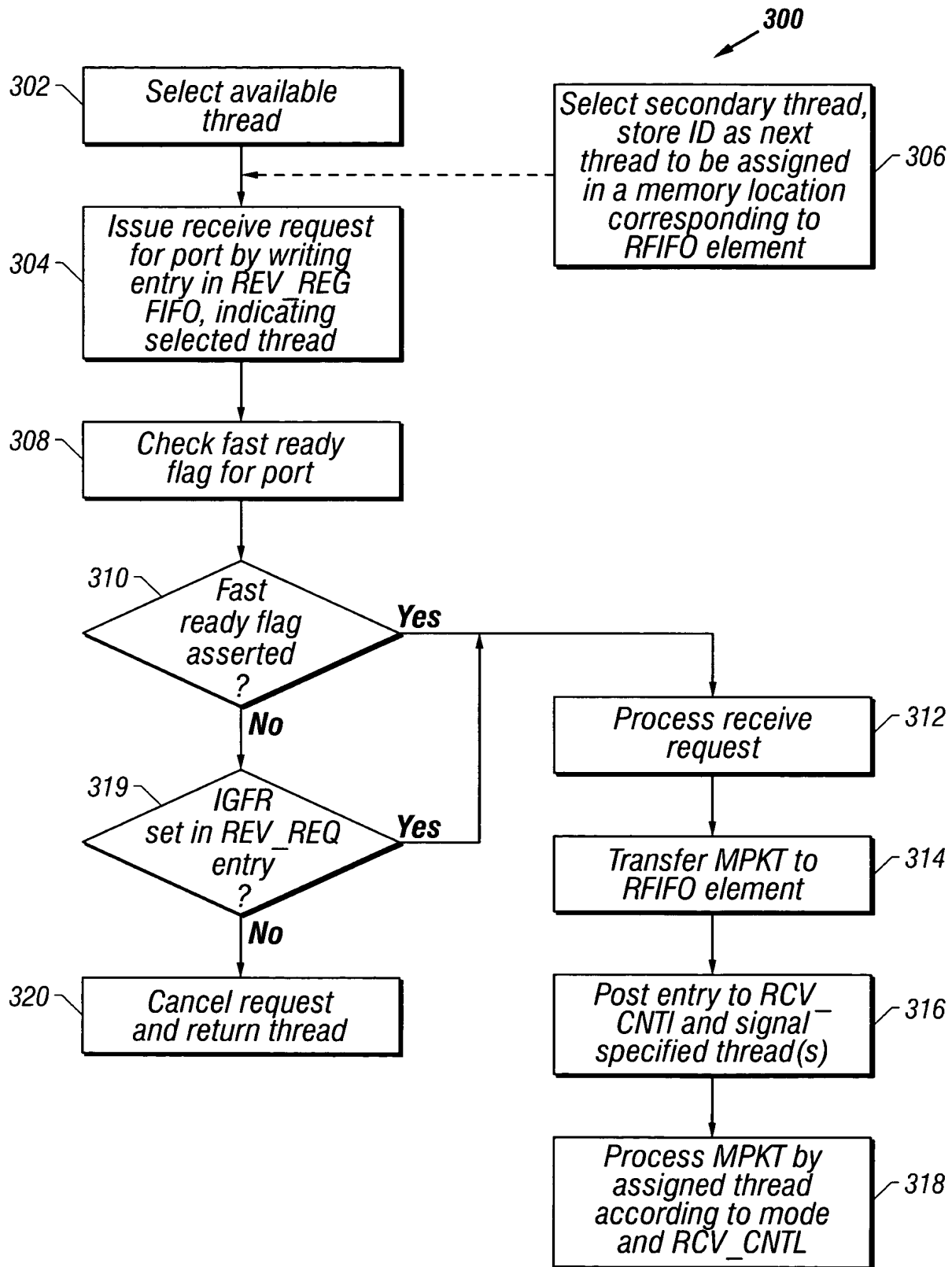


FIG. 12

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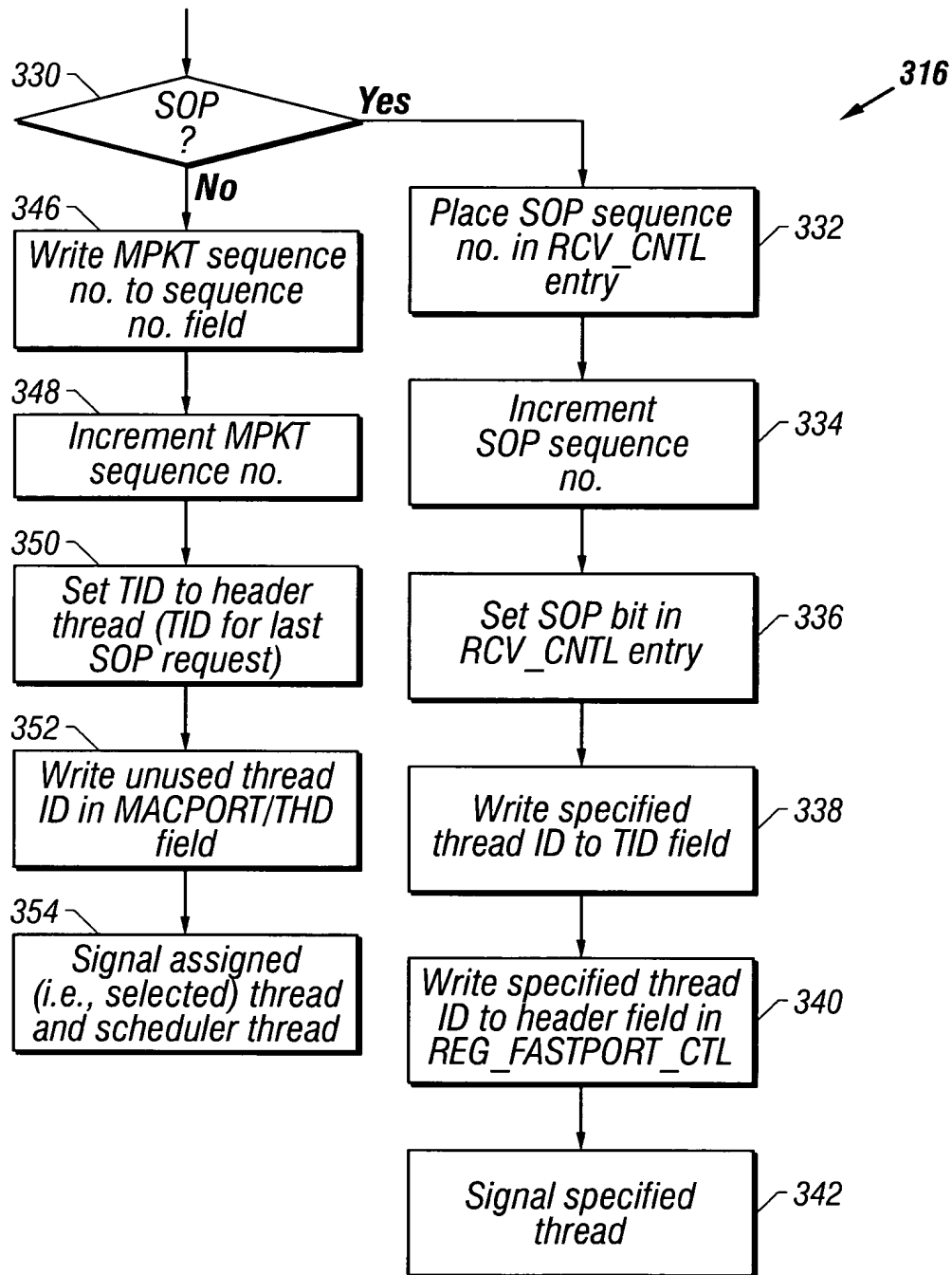


FIG. 13A

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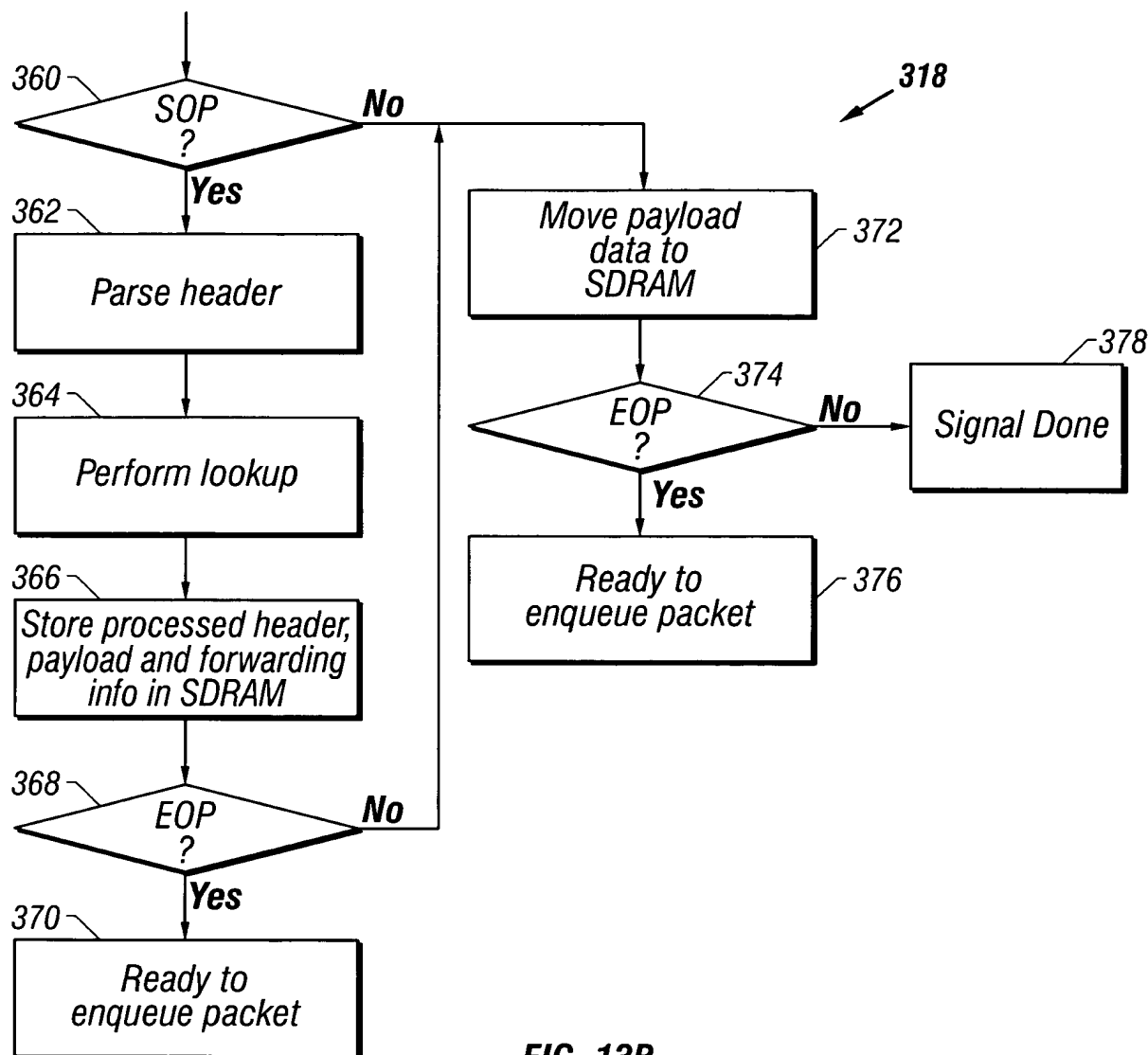
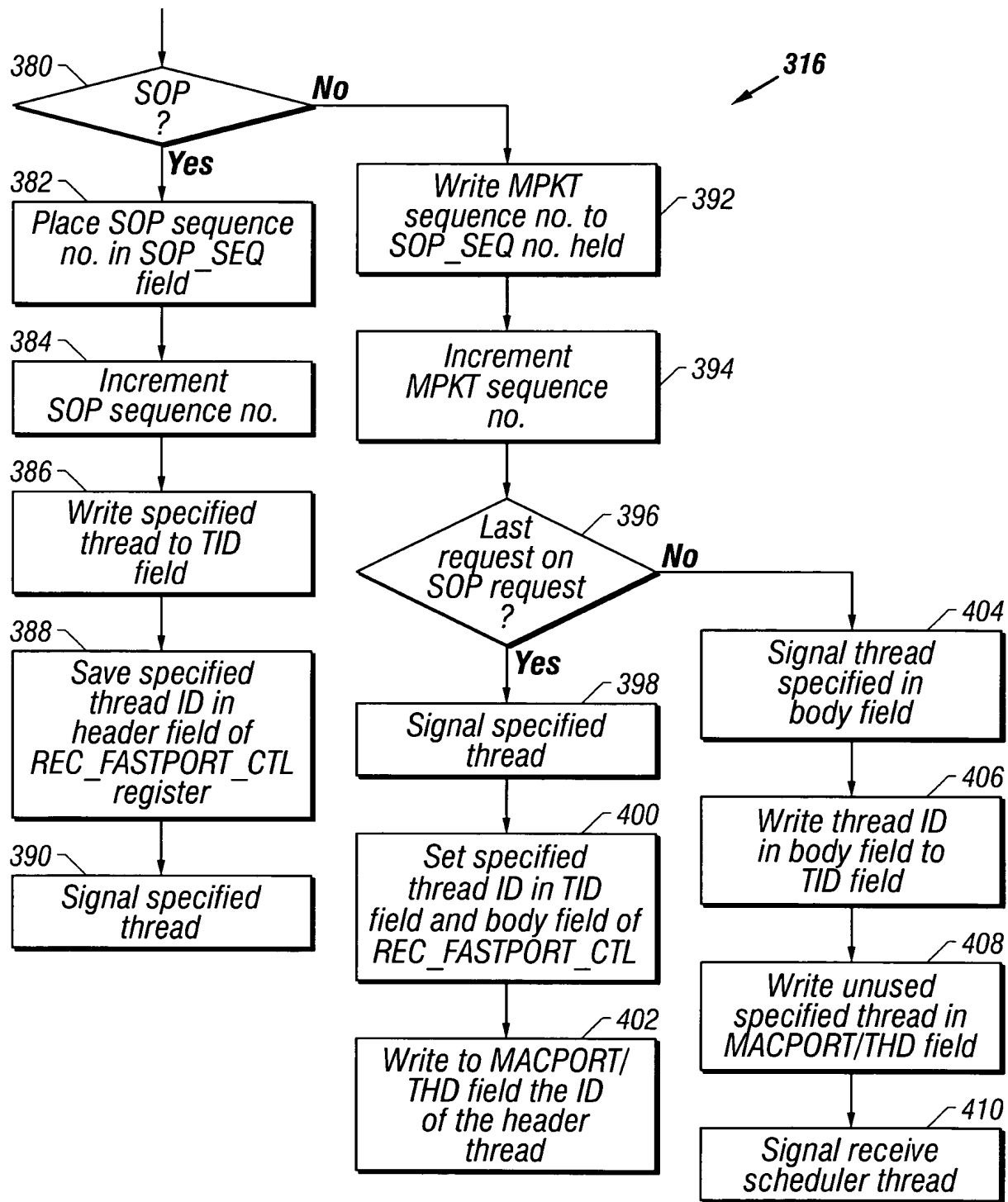


FIG. 13B

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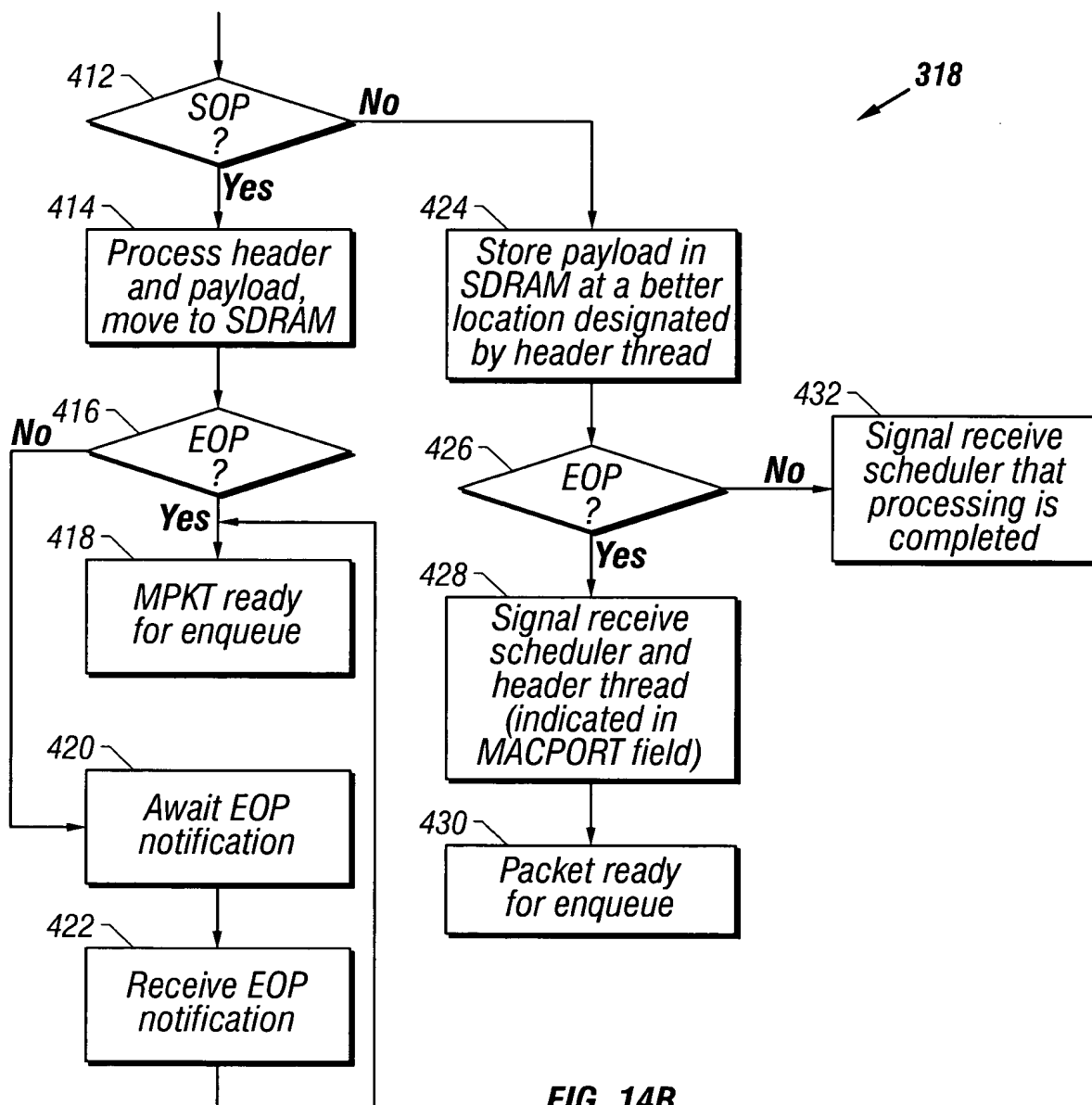


FIG. 14B

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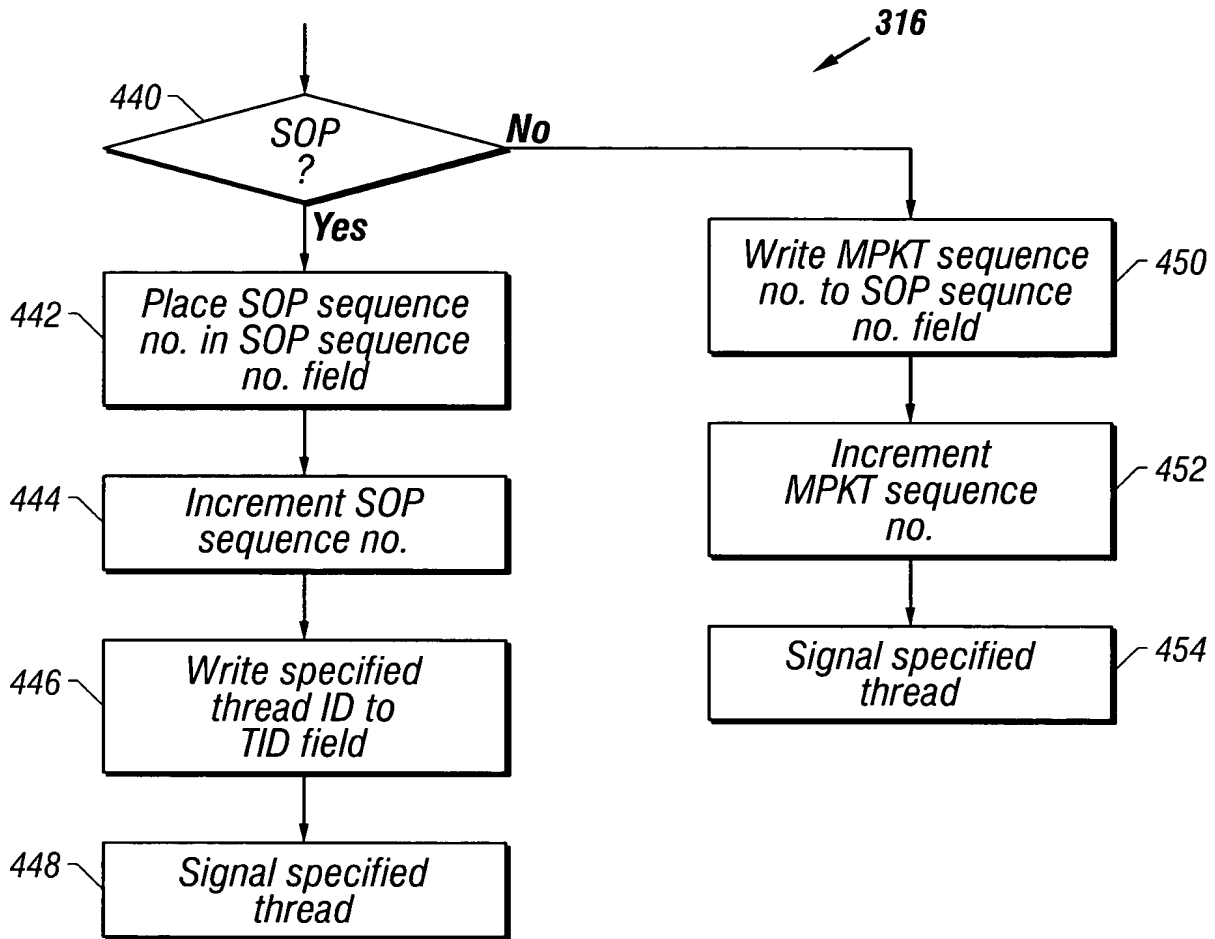
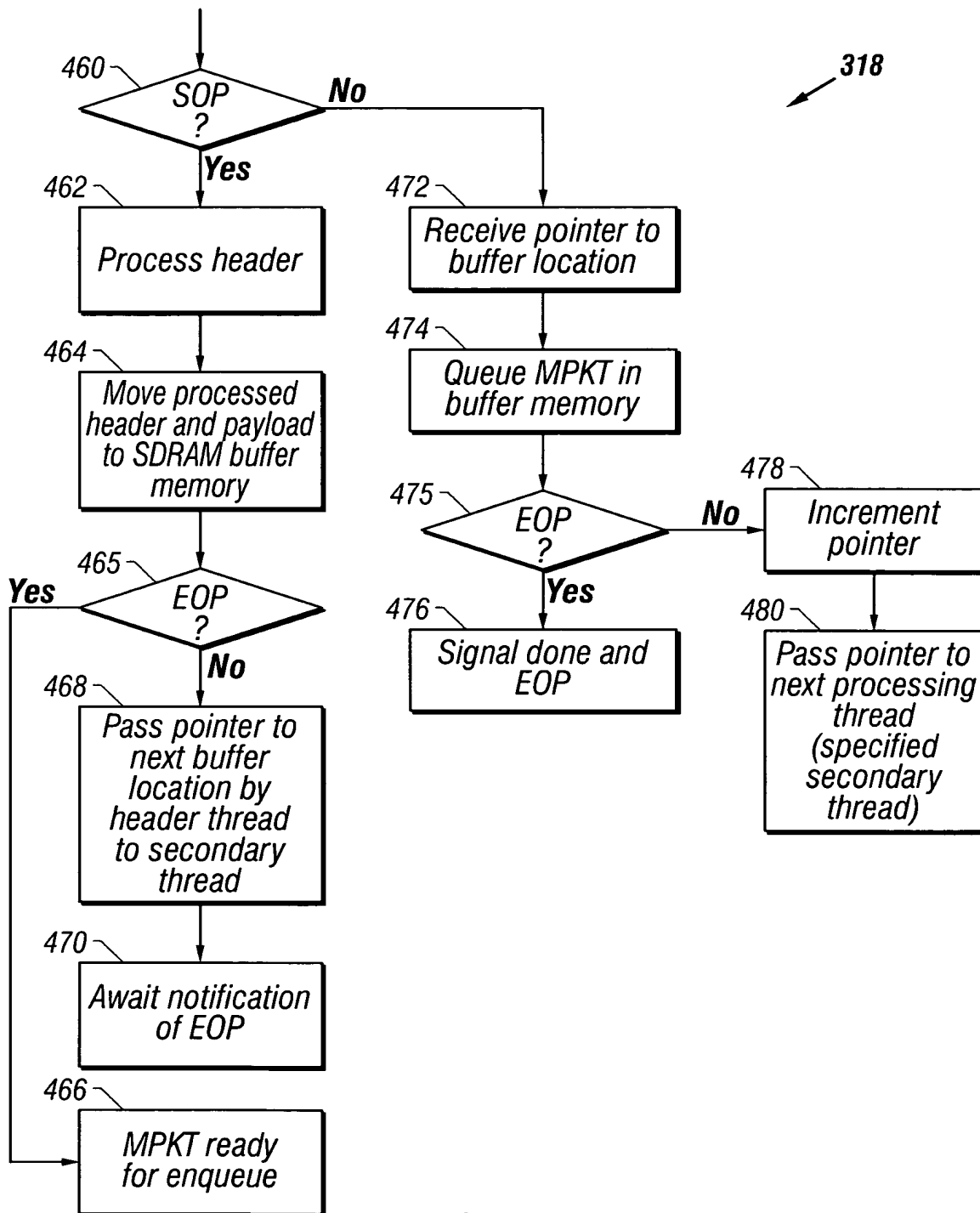


FIG. 15A

19/20**FIG. 15B**

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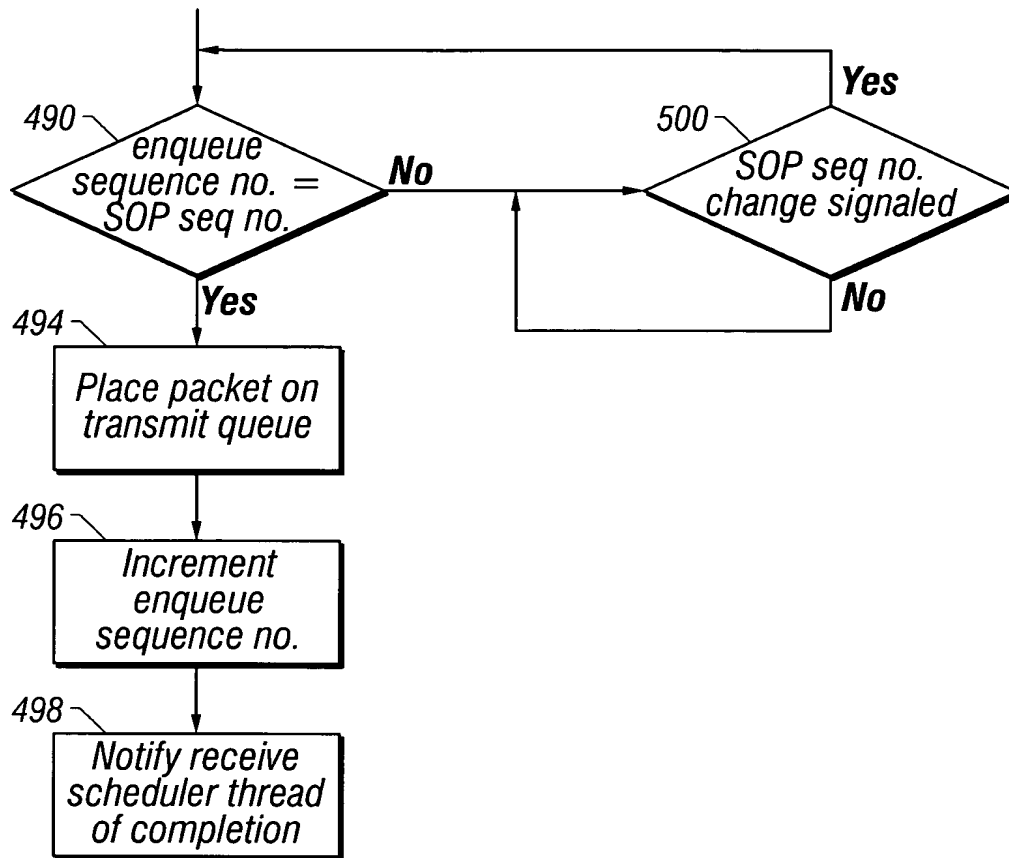


FIG. 16